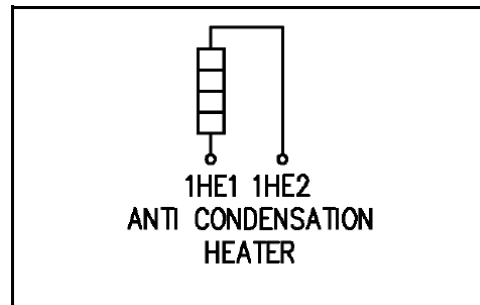
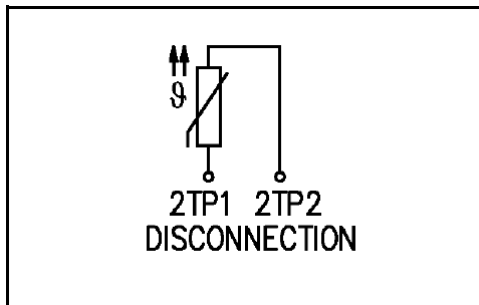
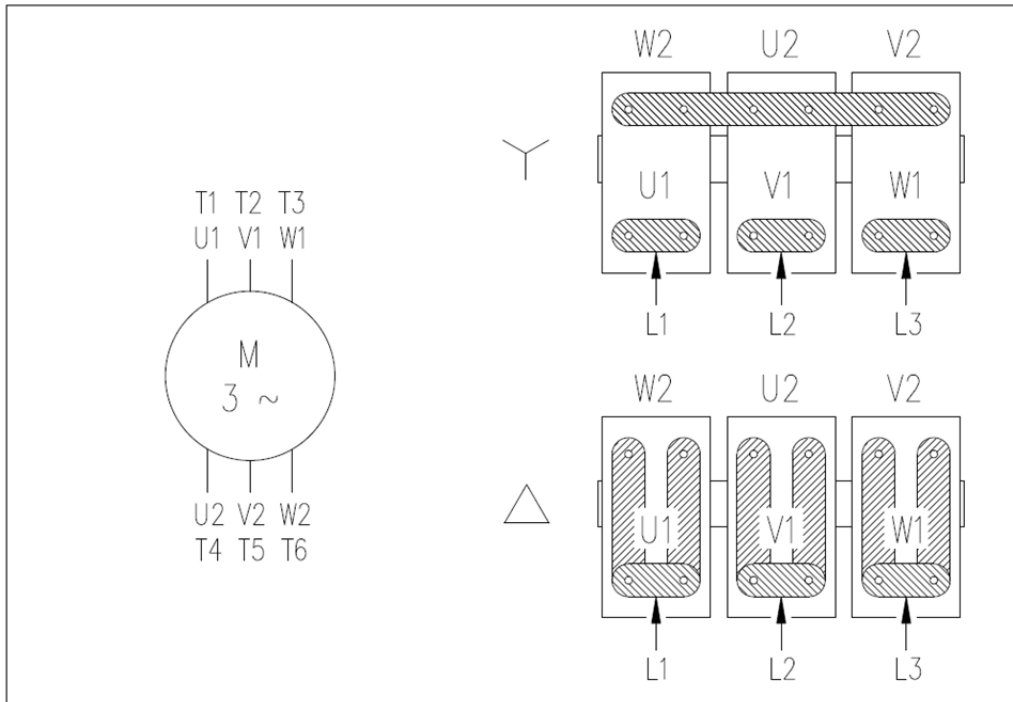


|                                     |   |                            |                                      |              |
|-------------------------------------|---|----------------------------|--------------------------------------|--------------|
| Verantwortliche Abt.<br>PD LD P R&D | Technische Referenz   | Erstellt von<br>DTK        | Genehmigt von                        | Projekt      |
| <b>SIEMENS</b>                      | Dokumenttyp<br>Circuit diagram                                  |                            | Dokumentstatus<br>released           |              |
|                                     | Titel<br>Circuit diagram main + MLFB15 (B) + Q0x<br>1L/1M/1P... |                            | Dokumentnummer<br>53258100600000_024 |              |
| © Siemens AG 2017                   | Rev.<br>AA  | Erstelldatum<br>20.05.2017 | Sprache<br>en                        | Blatt<br>1/1 |



|                                     |   |                            |                                    |              |
|-------------------------------------|---|----------------------------|------------------------------------|--------------|
| Verantwortliche Abt.<br>PD LD P R&D | Technische Referenz   | Erstellt von<br>DTK        | Genehmigt von                      | Projekt      |
| <b>SIEMENS</b>                      | Dokumenttyp<br>Circuit diagram                                  |                            | Dokumentstatus<br>released         |              |
|                                     | Titel<br>Circuit diagram main + MLFB15 (B) + Q0x<br>1L/1M/1P... |                            | Dokumentnummer<br>A5E41159500A_020 |              |
| © Siemens AG 2017                   | Rev.<br>AA  | Erstelldatum<br>20.05.2017 | Sprache<br>en                      | Blatt<br>1/1 |